

**FIRST IETE INTERNATIONAL CONFERENCE INDIA-2018 (IICI-2018) – AGENDA**  
**Venue: IETE, Bellary Road, Bengaluru**

**Pre-Conference Tutorials – 13<sup>th</sup> December 2018**

<b>Time</b>	<b>Session</b>	<b>Speaker</b>
09:30 – 10:00	Inauguration	Prof. K T V Reddy, President, IETE & Prof. M N Hooda, Chair TPPC, IETE
10:00 – 11:00	Evolution towards 5G and Ubiquitous Networks	Mr. Ravikanth Pasumarthy, Assistant Vice President, Aricent & Mr. Vinesh Varghese, Director Technology, Aricent
11:00 – 11:15	<i>Tea Break</i>	
11:15 – 12:15	Optical Networks, 5G RAN, 5G Standardization, IMT-2020	Mr. Jishnu Aravindakshan, Product Architect Technology, Tejas Networks
12:15 – 13:15	Massive MIMO	Mr. Divagar Bhaskaran, Head of 3GPP Delegates, Center Of Excellence In Wireless Technologies (CEWiT), IIT Madras
13:15 – 14:15	<i>Lunch Break</i>	
14:15 – 15:15	Real Time Network Analytics for 5G	Mr. Subhas Mondal, Chief Architect and Practice Head, Wipro
15:15 – 15:30	<i>Tea Break</i>	
15:30 – 16:30	IoT : Protocols, Challenges and Applications	Mr. Haribabu, Joint Director, C-DAC
16:30 – 17:30	Big Data Security and Privacy	Ms. Swapnil Shrivastava, Principal Tech. Officer, C-DAC

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Photo	Profile
	<p><b>Mr. Ravikanth Pasumarthy</b> Assistant Vice President, Aricent RaviKanth has around 23 years of experience in wireless and satellite based systems, is working as Assistant Vice President at Aricent, and is System architect in 4G and 5G related activities. Ravi has experience in successful conceptualization, architecture, and design to delivery of wireless base-stations of various form-factors (macro to small-cell) across GSM/EDGE, 3G and LTE areas for multiple customers. His area of expertise involves Solution engineering, System architecture of complete wireless systems, expertise in PHY and L2/L3 realization.</p>
	<p><b>Mr. Vinesh Varghese</b> <b>Director Technology, Aricent</b> As Director, Technology at Aricent Vinesh Varghese is charged with helping define future technology strategy for the Systems &amp; Silicon Design Practice. Activities which includes identifying and tracking key technical trends, performing technical due diligence, and supporting advanced technology adoption for the various ongoing Engineering programs. Additional responsibility includes also in designing system and helping chart, end use cases to the next generation communication platforms.</p>
	<p><b>Mr. Jishnu Aravindakshan</b> Jishnu Aravindakshan is Product Architect- Technology in CTO's office, Tejas Networks India Limited. He obtained his BTech in electronics and communication from NIT, Calicut in 2001 and Masters from IISc, Bangalore in 2004. Subsequently he joined Tejas Networks and is responsible for deciding the long-term roadmap of the company. He represents Tejas at ITU-T SG15 and has contributed towards the development of ITU-T G.8032 and MPLS-TP standards. His present interests include 5G, IoT, and SDN/NFV technologies.</p>
	<p><b>Mr. Dhivagar Baskaran</b> Head of 3GPP Delegates, at Centre of Excellence in Wireless Technology Lead Research Engineer of CEWiT, working on Physical Layer Standardization of 5G NR. Head of 3GPP Delegates representing CEWiT. Skilled in 5G NR, LTE, Receiver Algorithms, AAS, FD-MIMO. System Architect of Broadband Wireless Simulator from CEWiT.</p>
	<p><b>Mr. Subhas Mondal</b> Chief Architect and Practice Head, DTMS Technology, Wipro Limited Subhas has over 25 years of Engineering experience in Telecom R&amp;D. He is driving adoption of new technologies in transforming communications Networks spanning across 5G, SDN/NFV, LiFi, Security and AI based network automation solutions. He is architecting solutions on Digital Network Transformation theme in Wipro HOLMES platform.</p>

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**Mr. Haribabu**

Joint Director, C-DAC, Bangalore

Mr. Haribabu is working as Joint Director for IoT (Internet of Things) team at CDAC, Bangalore. He has completed M.Tech from Andhra University in the year 2000 and he is working with CDAC, Bangalore Since January 2001. He is currently pursuing PhD at IIIT, Bangalore. His areas of expertise are in VLSI design, embedded system development, Wireless sensor Networks, Energy harvesting for WSNs, FPGA design, Cryptography and Cryptanalysis. He has published more than 17 papers in International conferences and has two patents filed. He has conducted five national workshops in the area of wireless sensor networks. His research areas include energy aware routing protocols, advances in MAC protocols, automation systems for energy sector, Security in IoT. He is a senior member of IEEE.



**Ms. Swapnil Shrivastava**

Principal Technical Officer, C-DAC, Bangalore

With 18+ years of technological, research and managerial experience, she is presently working as Principal Technical Officer in Big Data Analytics group at C-DAC, Bangalore. She has contributed in various capacities and at different locations of C-DAC with 100% success rate in delivering on Research projects, Mission Mode Projects, Turnkey projects and Technical Consultancy for clients ranging from MEITY-GoI, UIDAI, BEL, SET India and International Agencies. She has international publications in reputed conferences to her credit. She has contributed in various professional courses and workshops. She is pursuing PhD in the area of Big Data Security and Privacy from IIIT Bangalore.